



Substitute for form 1449A/PTO

## **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

Substitute for form 1448A/PTO		<i>Complete if Known</i>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		Application Number	10/812,242
		Filing Date	March 29, 2004
		First Named Inventor	Chad Thomas Steward
		Art Unit	
		Examiner Name	
		Attorney Docket Number	55123P298
Sheet		of	

**NON-PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No.*	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
<i>BSP</i>		GREBENE, ALAN B., "Bipolar and MOS Analog Integrated Circuit Design", 1984, Micro-Linear Corporation, Sunnyvale, California, Pgs. 853-855 <i>No mon 4/1</i>

Examiner Signature		Date Considered	11/16/04
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*\*Disclaimer: Initial reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.*

*\*Applicant's unique citation designation number. \*Applicant is to place a check mark here if English language Translation is attached.*

Based on PTO/SB/088 (08-03) as modified by Blankley, Solotoff, Taylor & Zaitman (rev) 08/11/2003.  
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